

**Description**

This 40V,100A N-Channel MOSFET in a PDFN5\*6 Plastic Package.

**Features**

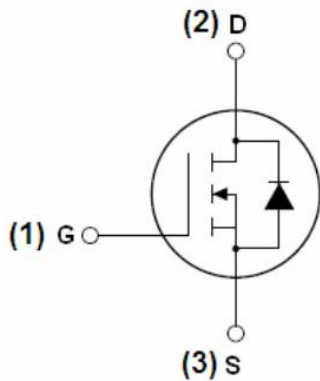
- Low  $R_{DS(ON)}$  to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance

**Applications**

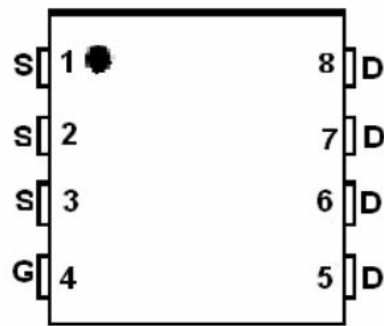
Battery Management

$V_{DSS}$	$R_{DS(on)}$ Typ	$I_D$
40V	1.2m $\Omega$	100A

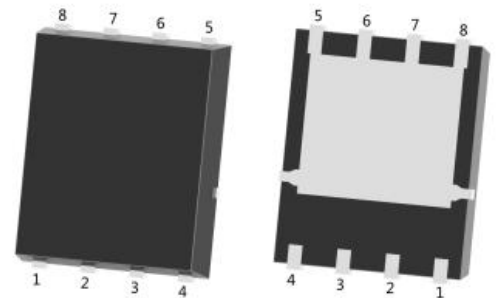
**Equivalent Circuit & Pinning**



**Schematic diagram**



**Pin assignment**



**PDFN5X6-8L**

**Absolute Maximum Ratings(Ta=25°C)**

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	40	V
Drain Current - Continuous	$I_D$	100	A
Drain Current – Pulsed	$I_{DM}$	230	A
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Power Dissipation	$P_D(T_c=25^\circ\text{C})$	114	W
Single Pulse Avalanche Energy(L=0.5mH)	$E_{AS}$	428.75	mJ
Avalanche Current(L=0.5mH)	$I_{AS}$	35	A
Junction and Storage Temperature Range	$T_j, T_{stg}$	-55 to 150	°C
Thermal resistance, junction - case	$R_{\theta JC}$	1.5	°C/W
Thermal resistance, junction - ambient	$R_{\theta JA}$	55	°C/W

**Electrical Characteristics(Ta=25°C)**

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$BV_{DSS}$	$I_D=250\mu\text{A}, V_{GS}=0\text{V}$	40	50		V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=40\text{V}, V_{GS}=0\text{V}$			1.0	$\mu\text{A}$
Gate-Body leakage current	$I_{GSS}$	$V_{DS}=0\text{V}, V_{GS}=\pm 20\text{V}$			$\pm 10$ 0	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1	1.45	2	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10\text{V}, I_D=10\text{A}$		1.2	1.5	m $\Omega$
		$V_{GS}=4.5\text{V}, I_D=10\text{A}$		1.6	2.0	
Input Capacitance	$C_{iss}$	$V_{DS}=25\text{V}, V_{GS}=0\text{V}$ $f=1.0\text{MHz}$		5800		pF
Output Capacitance	$C_{oss}$			1860		
Reverse Transfer Capacitance	$C_{rss}$			135		
Gate resistance	$R_g$	$V_{GS}=0\text{V}, V_{DS}=0\text{V}$ $f=1\text{MHz}$		1.25		$\Omega$
Total Gate Charge	$Q_g(10\text{V})$	$V_{GS}=10\text{V}, V_{DS}=20\text{V},$ $I_D=20\text{A}$		70		nC
Total Gate Charge	$Q_g(4.5\text{V})$			30		
Gate Source Charge	$Q_{gs}$			14.5		
Gate Drain Charge	$Q_{gd}$			10.5		
Turn-On Delay Time	$t_{d(on)}$	$V_{GS}=10\text{V}, V_{DS}=20\text{V},$ $R_L=1\Omega, R_{GEN}=3\Omega$		15		ns
Turn-On Rise Time	$t_r$			5.8		
Turn-Off Delay Time	$t_{d(off)}$			62		
Turn-Off Fall Time	$t_f$			10		

Electrical Characteristic Curve

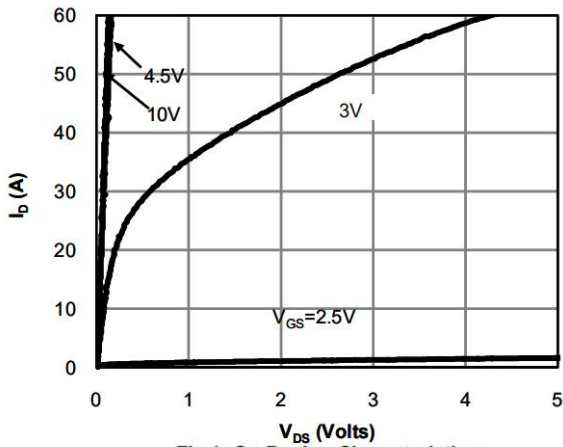


Fig 1: On-Region Characteristics

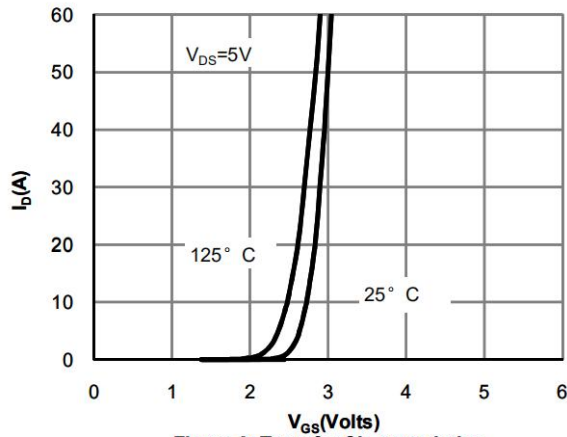


Figure 2: Transfer Characteristics

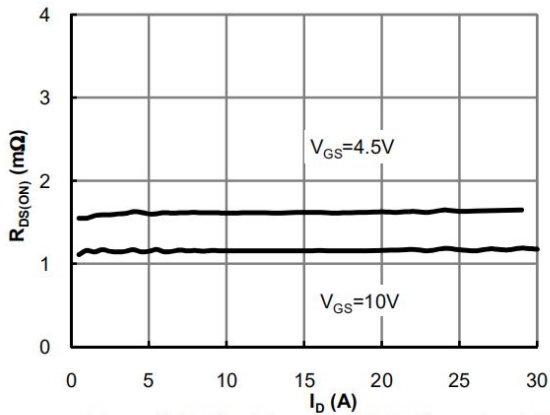


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

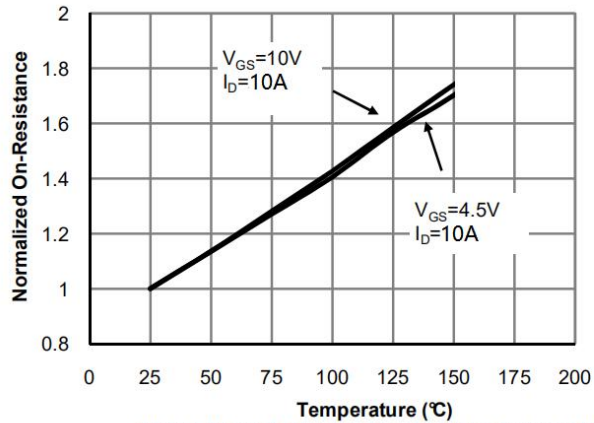


Figure 4: On-Resistance vs. Junction Temperature

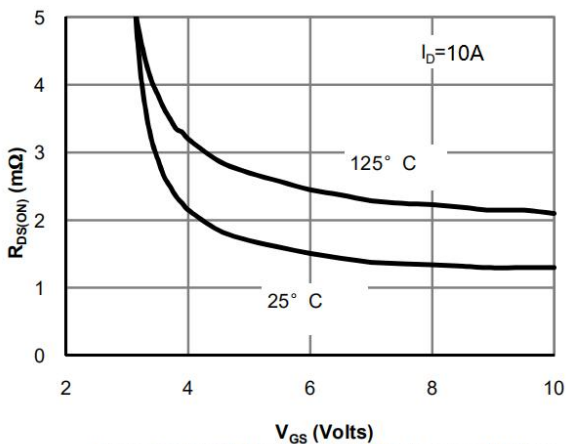


Figure 5: On-Resistance vs. Gate-Source Voltage

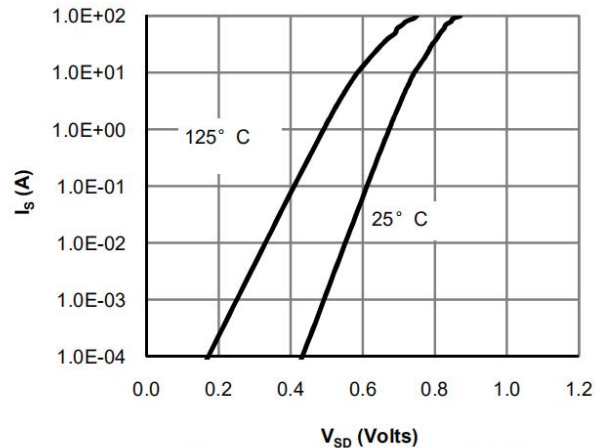
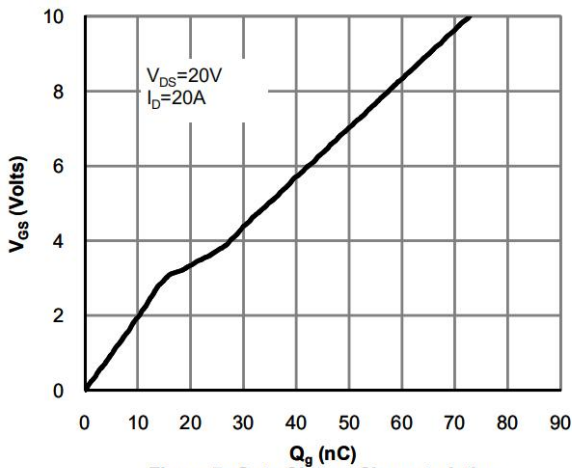
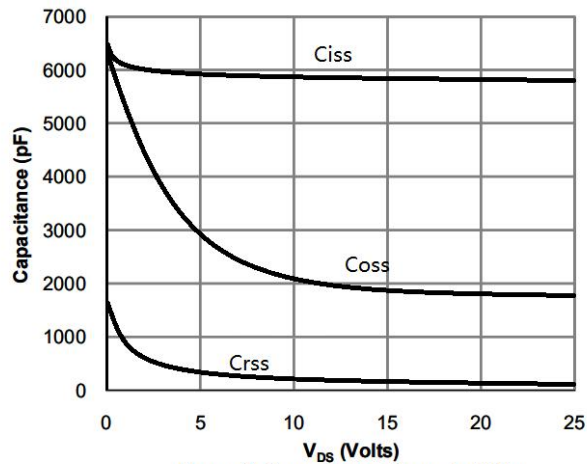


Figure 6: Body-Diode Characteristics

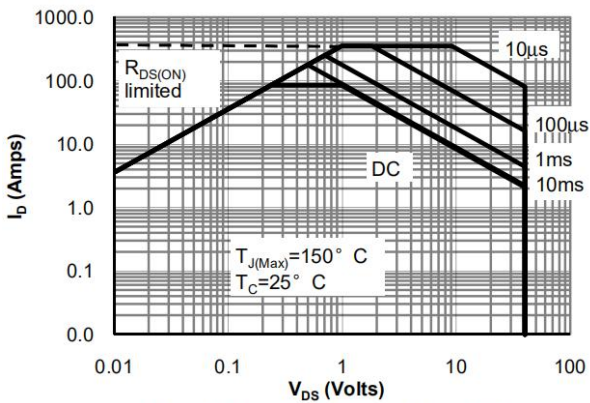
**Electrical Characteristic Curve**



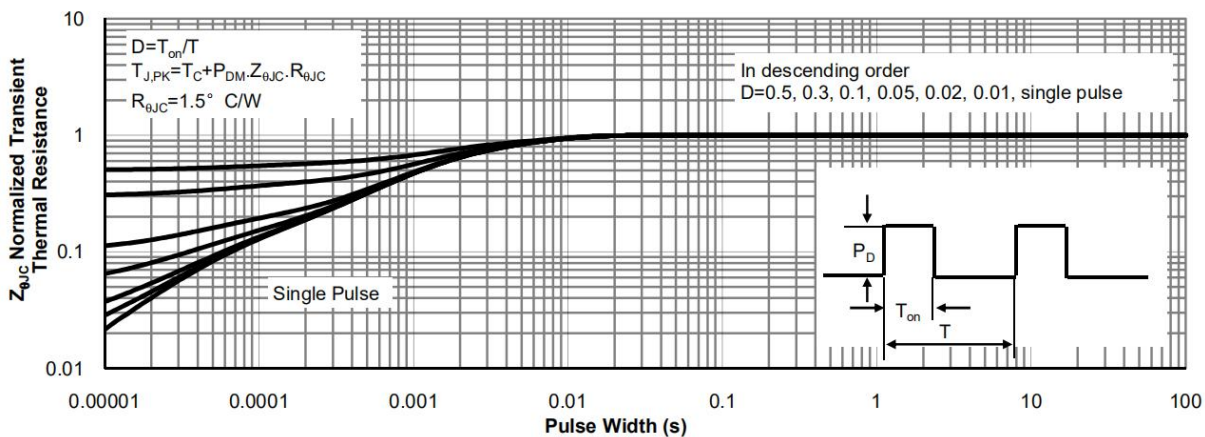
**Figure 7: Gate-Charge Characteristics**



**Figure 8: Capacitance Characteristics**

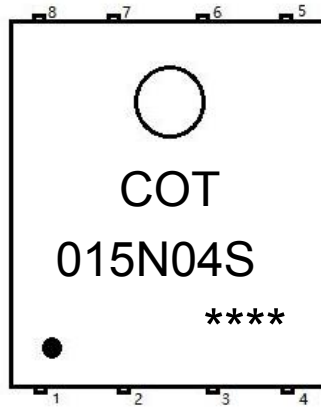


**Figure 9: Maximum Forward Biased Safe Operating Area**



**Figure 10: Normalized Maximum Transient Thermal Impedance**

Marking Instructions



Note:

COT: Company Logo

015N04S: Product Type.

\*\*\*\*: Lot No. Code, code change with Lot No.

Packaging SPEC.

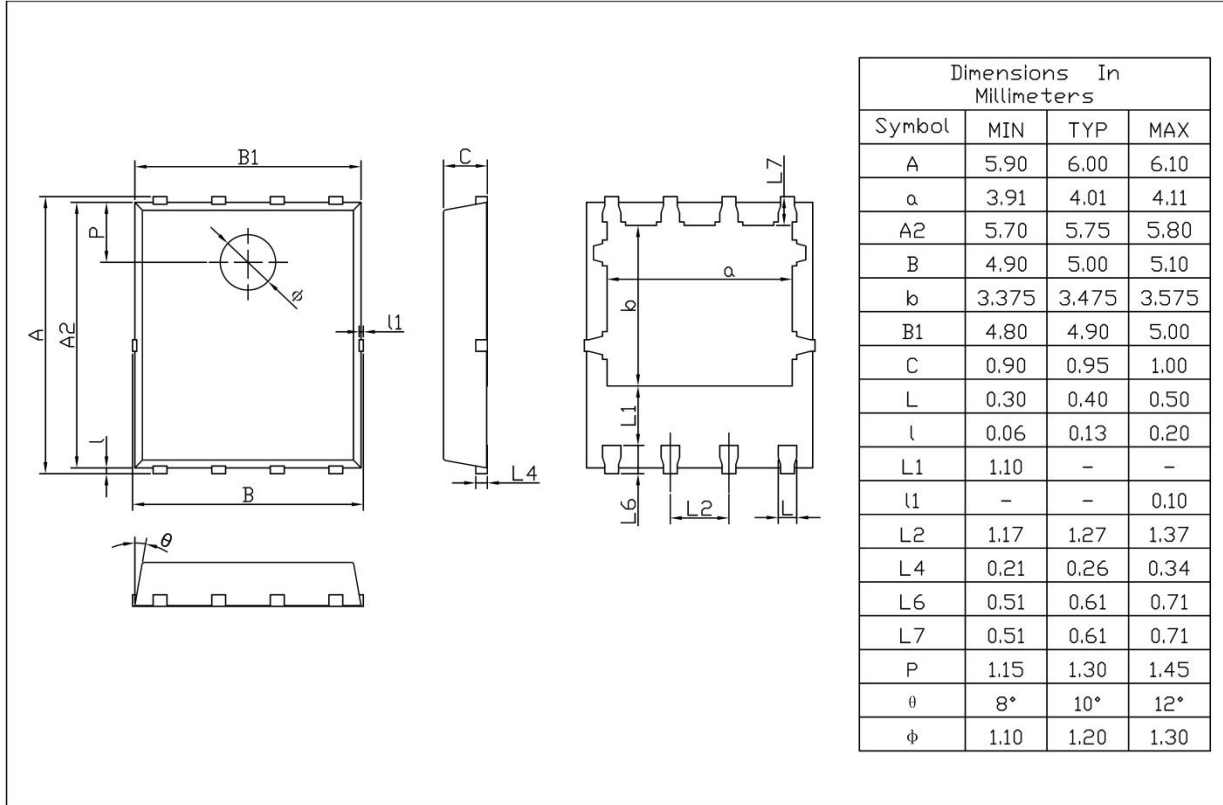
REEL INFORMATION

Package Type	Units					Dimension (unit: mm <sup>3</sup> )		
	Units/Reel	Reels/Inner Box	Units/Inner Box	Inner Boxes/Outer Box	Units/Outer Box	Reel	Inner Box	Outer Box
PDFN5*6	5000	2	10000	6	60000	13" × 12	360 × 360 × 50	380 × 335 × 366

Package Outline Dimensions

PDFN5 X6

Unit:mm



Rev.01 202209