

Description

This is 40V 70A N-Channel Enhancement Mode Power MOSFET in a PDFN5060-8L plastic package.

Uses advanced power trench technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

Features

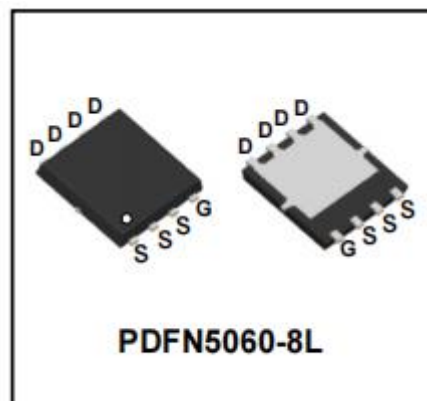
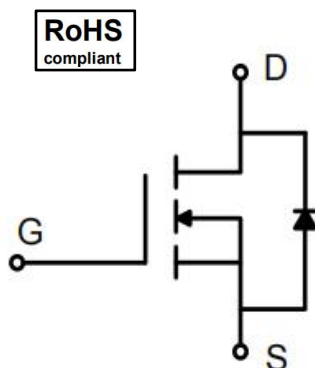
- $V_{DS} = 40V$, $I_D = 70A$
 $R_{DS(on)} < 6.5m\Omega$ @ $V_{GS} = 10V$
 $R_{DS(on)} < 8.5m\Omega$ @ $V_{GS} = 4.5V$
- Green Device Available
- Low Gate Charge
- 100% EAS Guaranteed

Applications

- Battery Management
- Motor Control and Drive
- UPS

V_{DSS}	$R_{DS(on)}$ (typ)	I_D
40 V	4.7m Ω	70A

Equivalent Circuit & Pinning



Absolute Maximum Ratings(Ta=25°C)

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	40	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current ¹	T _C =25°C	I_D	70	A
	T _C =100°C		56	
Pulsed Drain Current ²		I_{DM}	146	A
Single Pulse Avalanche Energy ³		EAS	88.2	mJ
Avalanche Current		I_{AS}	42	A
Total Power Dissipation ⁴	T _C =25°C	P_D	51	W
Operating Junction and Storage Temperature Range		T_J , T_{STG}	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient ¹	R_{θJA}	61	°C/W
Thermal Resistance from Junction-to-Case ¹	R_{θJC}	2.5	°C/W

Electrical Characteristics (T_J = 25°C, unless otherwise noted)

Parameter		Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics							
Drain-Source Breakdown Voltage		V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	40	-	-	V
Gate-body Leakage Current		I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current	T _J =25°C	I _{DSS}	V _{DS} = 32V, V _{GS} = 0V	-	-	1	μA
	T _J =55°C			-	-	5	
Gate-Threshold Voltage		V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1.0	1.5	2.5	V
Drain-Source On-Resistance ²		R _{DS(on)}	V _{GS} = 10V, I _D = 10A	-	4.7	6.5	mΩ
			V _{GS} = 4.5V, I _D = 5A	-	6.2	8.5	
Forward Transconductance ²		g _{fs}	V _{DS} = 10V, I _D = 5A	-	26	-	S
Dynamic Characteristics							
Input Capacitance		C _{iss}	V _{DS} = 20V, V _{GS} =0V, f =1MHz	-	3260	-	pF
Output Capacitance		C _{oss}		-	245	-	
Reverse Transfer Capacitance		C _{rss}		-	185	-	
Switching Characteristics							
Gate Resistance		R _g	V _{DS} = 0V, V _{GS} =0V, f =1MHz	-	0.65	-	Ω
Total Gate Charge		Q _g	V _{GS} = 4.5V,V _{DS} = 20V, I _D = 10A	-	21	-	nC
Gate-Source Charge		Q _{gs}		-	5.7	-	
Gate-Drain Charge		Q _{gd}		-	9.6	-	
Turn-On Delay Time		t _{d(on)}	V _{GS} =10V, V _{DD} = 15V, R _G = 3.3Ω, I _D = 1A	-	15	-	nS
Rise Time		t _r		-	8.7	-	
Turn-Off Delay Time		t _{d(off)}		-	73	-	
Fall Time		t _f		-	7.2	-	
Drain-Source Body Diode Characteristics							
Diode Forward Voltage ²		V _{SD}	I _S = 1A, V _{GS} = 0V	-	-	1	V
Continuous Source Current ^{1,5}		I _S	V _G =V _D =0V , Force Current	-	-	70	A

Notes:

1. The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper.
2. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
3. The EAS data shows Max. rating. The test condition is V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=42A
4. The power dissipation is limited by 150°C junction temperature
5. The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

Typical Characteristics

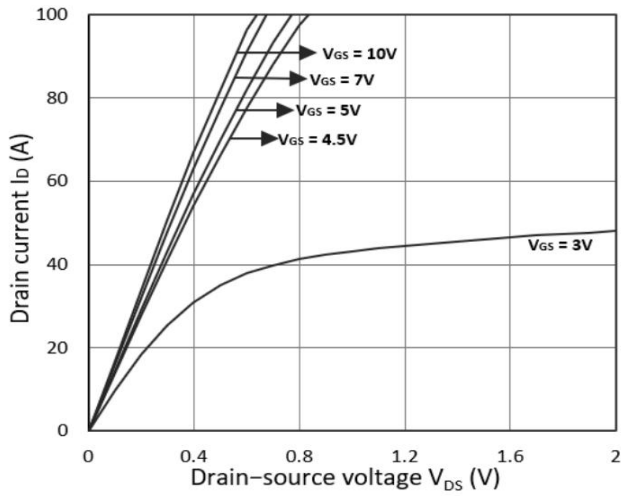


Figure 1. Output Characteristics

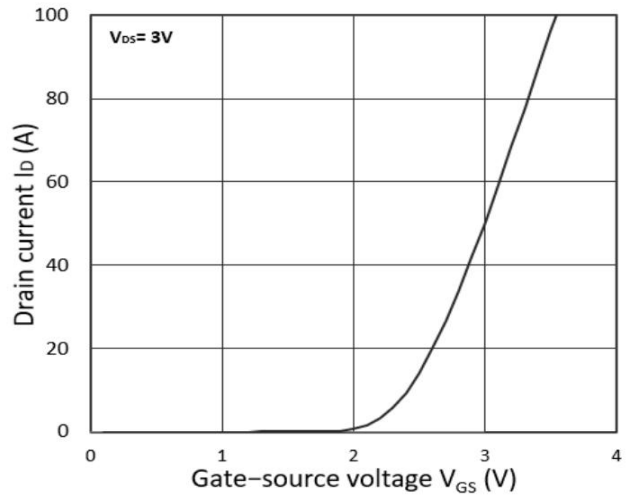


Figure 2. Transfer Characteristics

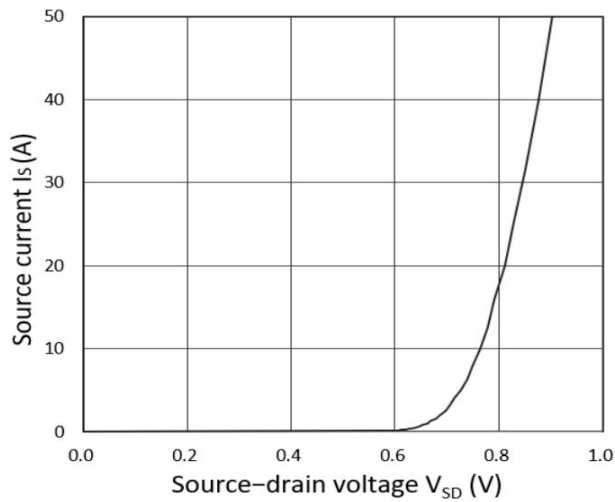


Figure 3. Forward Characteristics of Reverse

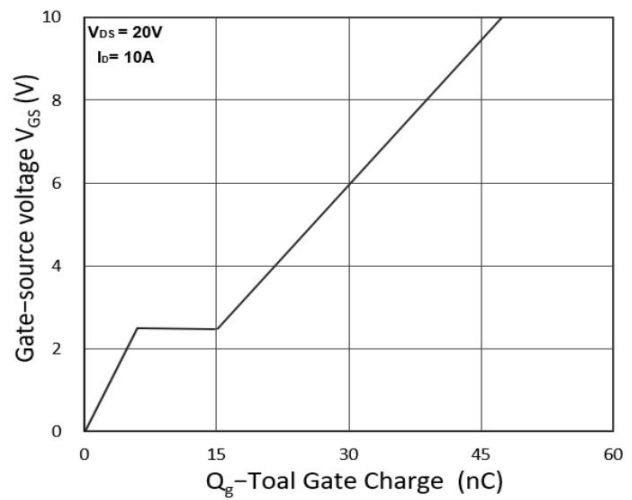


Figure 4. Gate Charge Characteristics

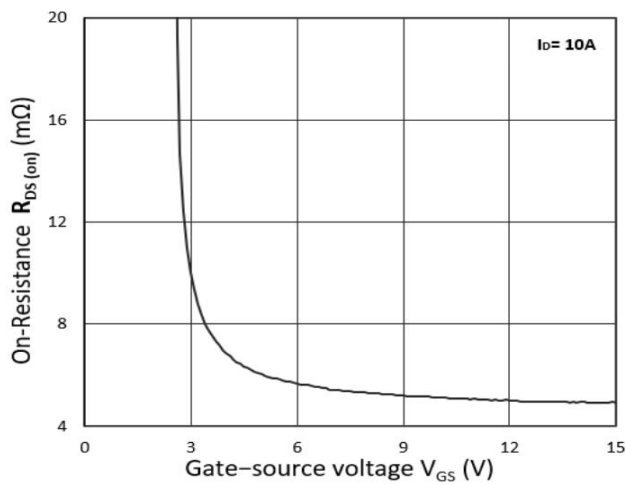


Figure 5. $R_{DS(on)}$ vs. V_{GS}

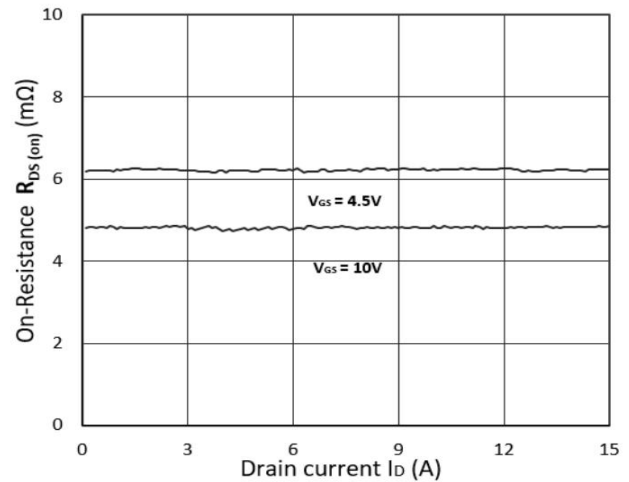


Figure 6. $R_{DS(on)}$ vs. I_D

Typical Characteristics

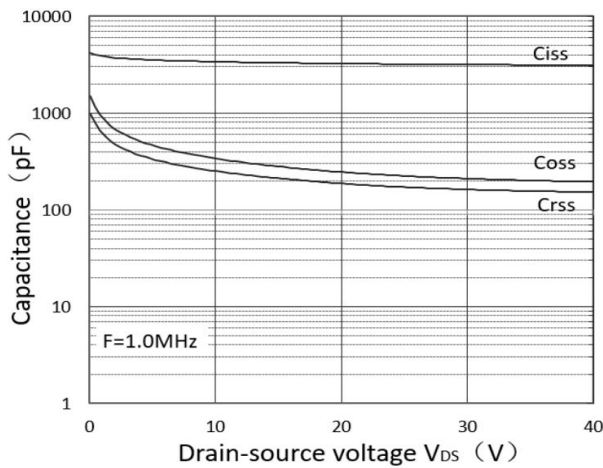


Figure 7. Capacitance Characteristics

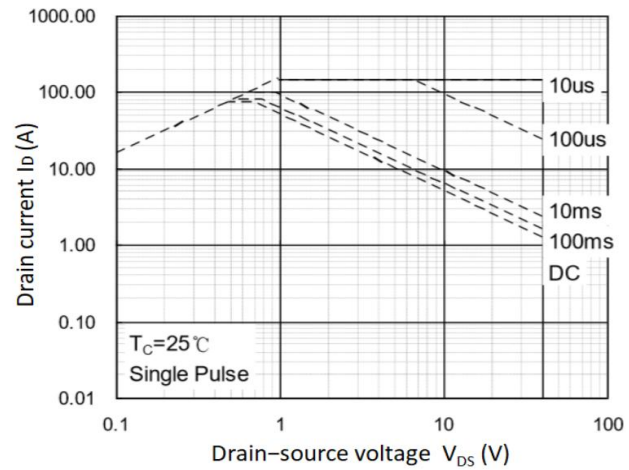


Figure 8. Safe Operating Area

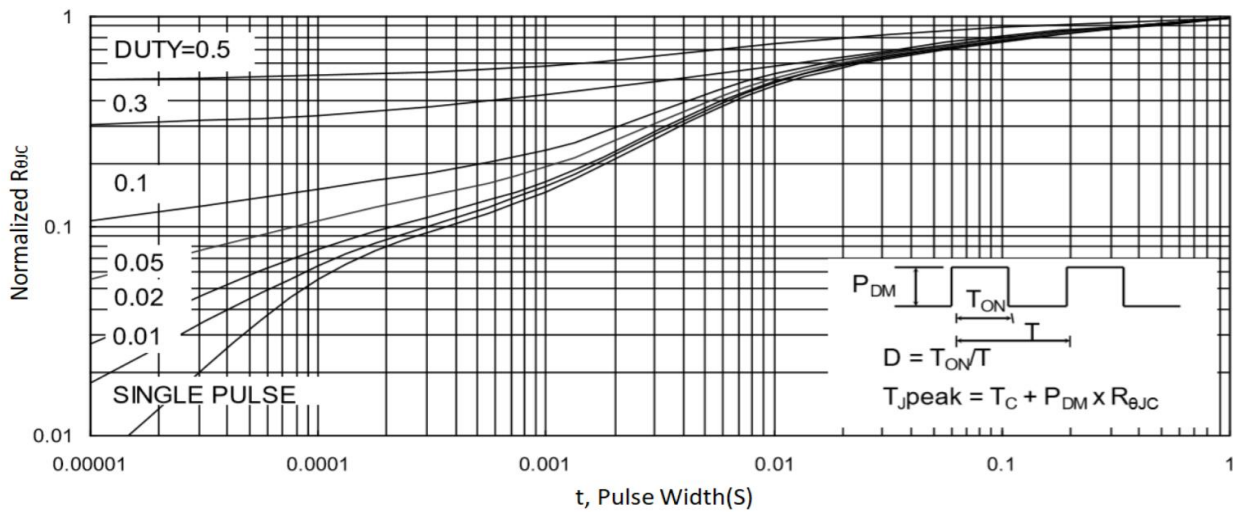


Figure 9. Normalized Maximum Transient Thermal Impedance

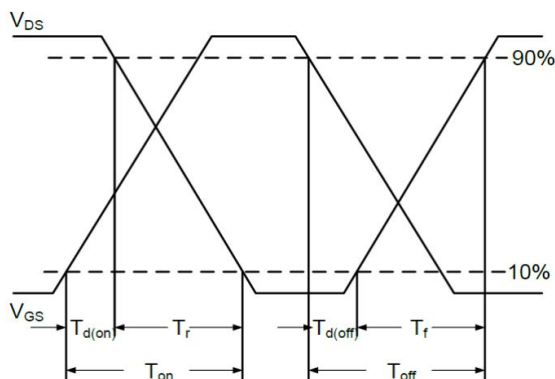


Figure 10. Switching Time Waveform

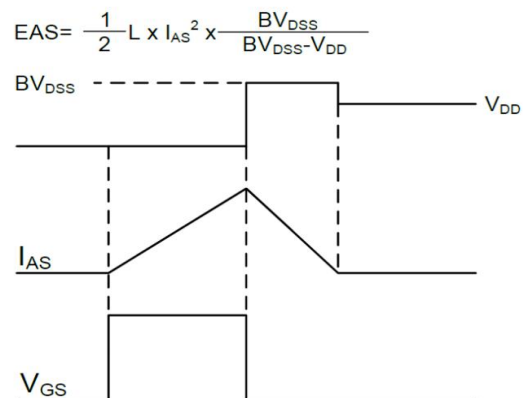
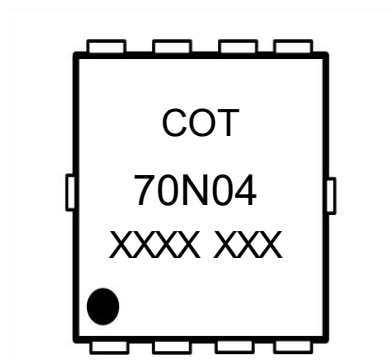


Figure 11. Unclamped Inductive Switching
Waveform

Marking Instructions

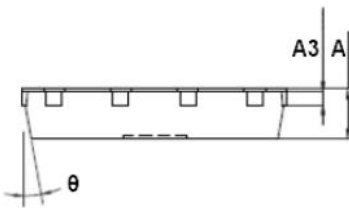
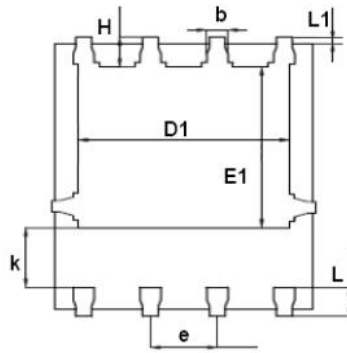
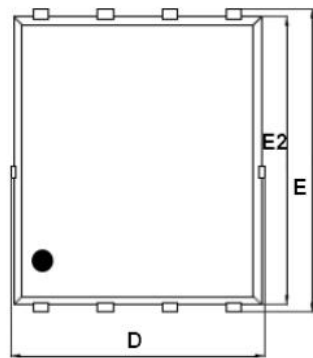


70N04 = Device code
XXXX XXX= Date code

Ordering Information

Part	Package	Marking	Packing method
CT70N04ZC	PDFN5060-8L	70N04	Tape and Reel

Mechanical Dimensions for PDFN5060-8L



COMMON DIMENSIONS

SYMBOL	MM	
	MIN	MAX
A	0.90	1.20
A3	0.15	0.35
D	4.80	5.40
E	5.90	6.35
D1	3.61	4.31
E1	3.30	3.92
E2	5.65	6.06
k	1.10	-
b	0.30	0.51
e	1.27BSC	
L	0.38	0.71
L1	0.05	0.36
H	0.38	0.61
θ	0°	12°