

## Descriptions

This is N-Channel Enhancement Mode Field Effect Transistor in a SOP-8 Plastic Package.

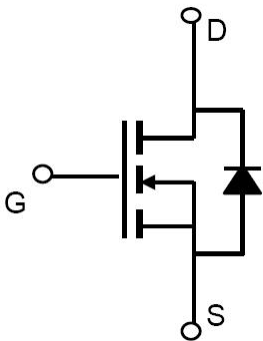
## Features

Uses advanced trench technology to provide excellent RDS(ON) with low gate charge. Halogen-free Product.

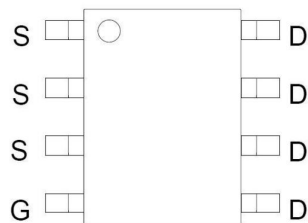
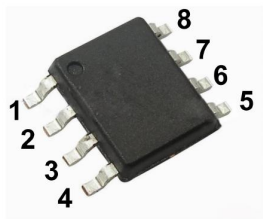
## Applications

Suitable for use in a wide range of power conversion applications.

## Equivalent Circuit



## Pinning



PIN1、PIN 2、PIN 3 : S      PIN 4 : G

PIN5、PIN 6、PIN 7、PIN 8 : D

## Marking

See Marking Instructions.

### Absolute Maximum Ratings(Ta=25°C)

Parameter	Symbol	Rating		Unit	
		10Sec	Steady State		
Drain-Source Voltage	$V_{DSS}$	40		V	
Gate-Source Voltage	$V_{GS}$	±20		V	
Drain Current –Continuous <sup>A</sup>	$I_D$	$T_A=25^{\circ}C$	13.5	10	A
		$T_A=70^{\circ}C$	10.8	8.0	A
Pulsed Drain Current <sup>B</sup>	$I_{DM}$	120		A	
Avalanche Current <sup>G</sup>	$I_{AR}$	23		A	
Repetitive avalanche energy L=0.3mH <sup>G</sup>	$E_{AR}$	79		mJ	
Power Dissipation for Single Operation <sup>A</sup>	$P_D$	$T_A=25^{\circ}C$	3.1	1.7	W
		$T_A=70^{\circ}C$	2.0	1.1	
Thermal Resistance, Junction-to-Ambient <sup>A</sup>	$R_{\theta JA}$	t≤10S	40		°C/W
		Steady State	75		°C/W
Thermal Resistance, Junction-to Lead <sup>A</sup> (Steady State)	$R_{\theta JI}$	24		°C/W	
Operating and Junction Temperature Range	$T_j T_{stg}$	-55~150		°C	

### Electrical Characteristics(Ta=25°C)

Parameter	Symbol	Test Conditions		Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V$	$I_D=250\mu A$	40			V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=40V$	$V_{GS}=0V$			1.0	μA
		$V_{DS}=40V$ $T_J=55^{\circ}C$	$V_{GS}=0V$			5.0	μA
Gate-Body Leakage Current Forward	$I_{GSS}$	$V_{GS}=\pm 20V$	$V_{DS}=0V$			±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$	$I_D=250\mu A$	1.0	1.4	2.0	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V$	$I_D=10A$		8.2	10	mΩ
		$V_{GS}=10V$ $T_J=125^{\circ}C$	$I_D=10A$		12.5	16	
		$V_{GS}=4.5V$	$I_D=8.0A$		12	15	
Diode Forward Voltage	$V_{SD}$	$I_S=1A$	$V_{GS}=0V$		0.72	1.0	V
Forward Transconductance	$g_{FS}$	$V_{DS}=5V$	$I_D=5A$		10		S

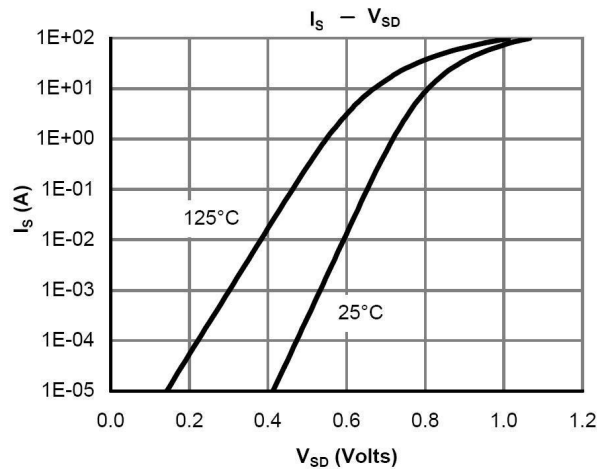
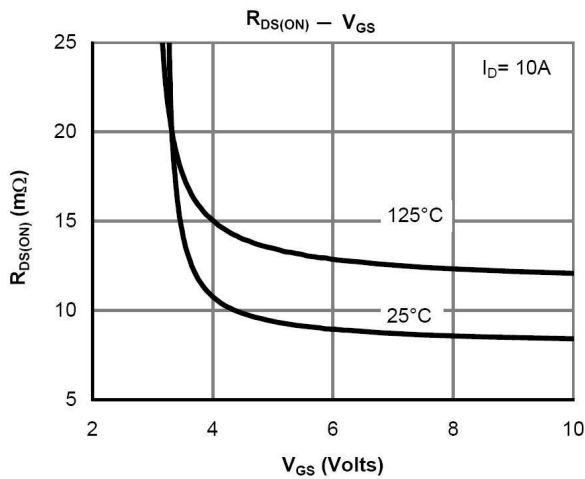
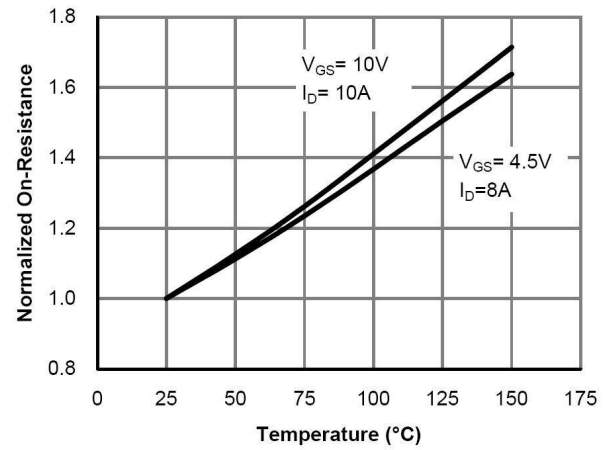
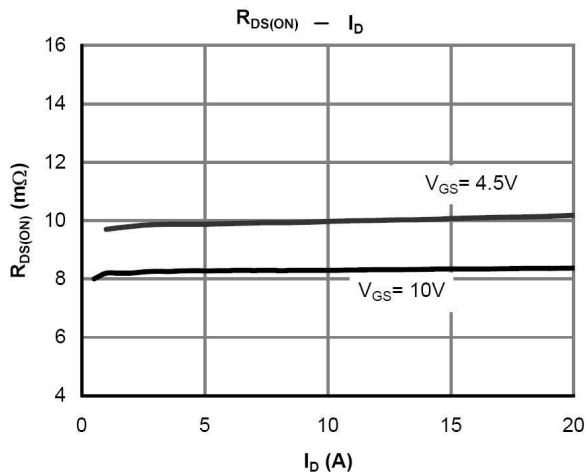
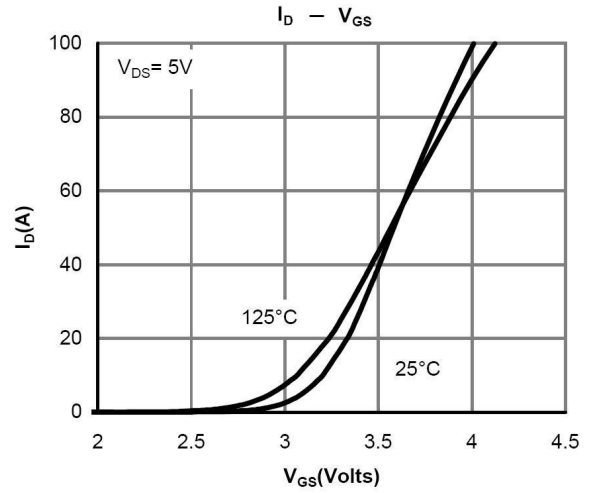
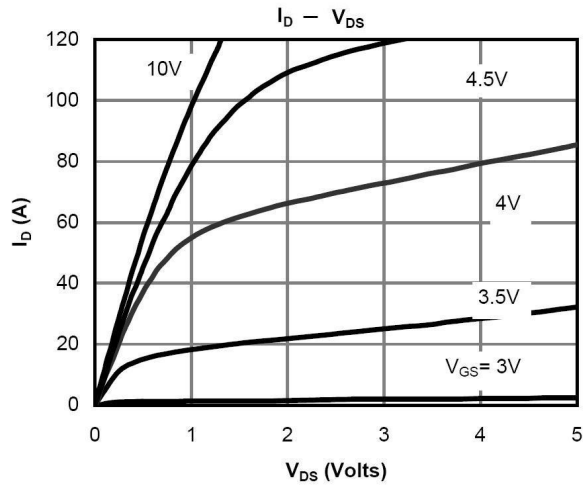
Electrical Characteristics(Ta=25°C)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit	
Input Capacitance	$C_{iss}$	$V_{DS}=15V$ $f=1.0MHz$ $V_{GS}=0V$		1500	1950	pF	
Output Capacitance	$C_{oss}$			215			
Reverse Transfer Capacitance	$C_{rss}$			135			
Total Gate Charge(10V)	$Q_g$	$V_{DS}=20V$ $V_{GS}=10V$ $I_D=10A$		27.2	37	nC	
Total Gate Charge(4.5V)				13.6	18		
Gate-Source Charge			$Q_{gs}$		4.5		
Gate-Drain Charge			$Q_{gd}$		6.4		
Gate resistance	$R_g$	$V_{GS}=0V$ $f=1MHz$ $V_{DS}=0V$	2.0	3.5	5.0	$\Omega$	
Turn-On Delay Time	$t_{d(on)}$	$V_{DS}=20V$ $V_{GS}=10V$ $R_L=2\Omega$ $R_{GEN}=3\Omega$		6.4		ns	
Turn-On Rise Time	$t_r$			17.2			
Turn-Off Delay Time	$t_{d(off)}$			29.6			
Turn-Off Fall Time	$t_f$			16.8			
Continuous Drain-Source Diode Forward Current	$I_s$				2.5	A	
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F=10A$ $di/dt=100A/\mu s$		30	40	ns	
Body Diode Reverse Recovery Charge	$Q_{rr}$			19		nC	

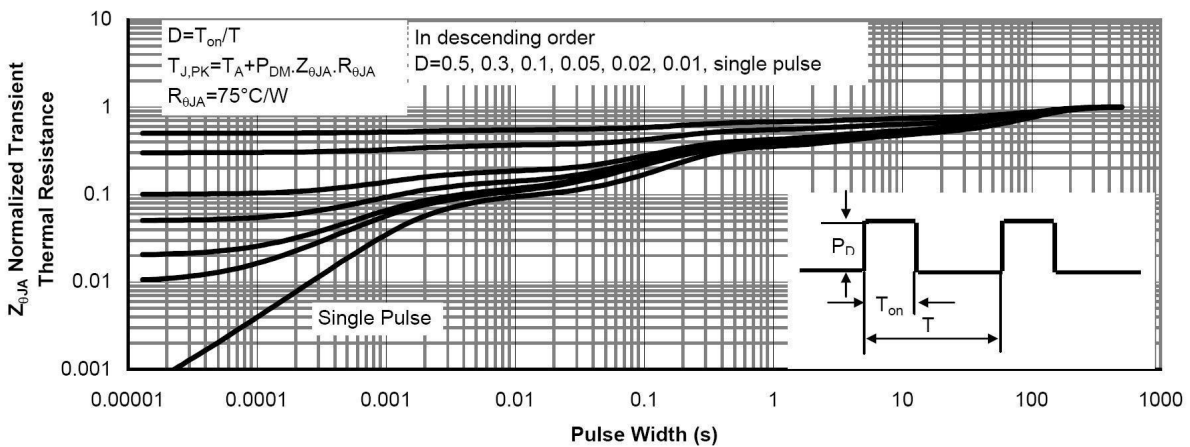
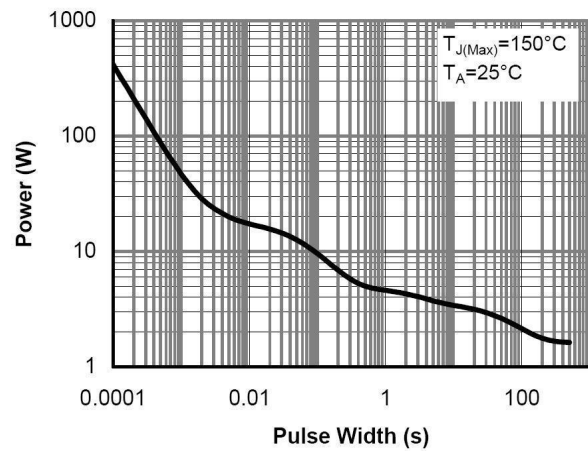
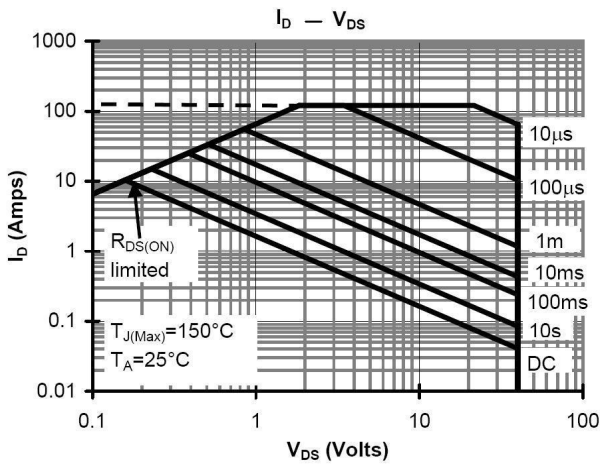
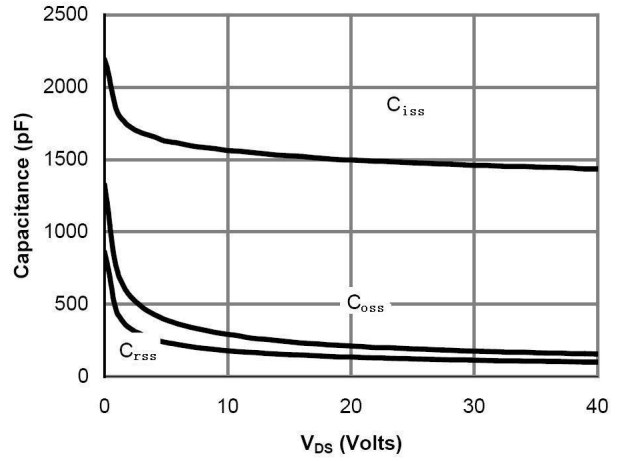
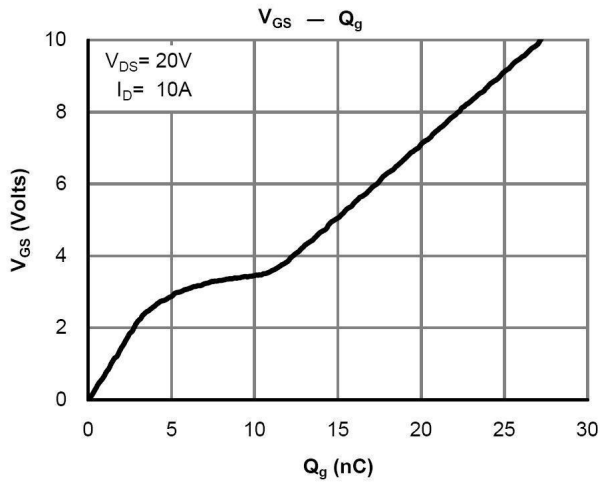
Notes:

- A: The value of  $R_{\theta JA}$  is measured with the device mounted on 1in2 FR-4 board with 2oz. Copper, in a still air environment with  $T_A=25^\circ C$ . The value in any given application depends on the user's specific board design.
- B: Repetitive rating, pulse width limited by junction temperature.
- C. The  $R_{\theta JA}$  is the sum of the thermal impedance from junction to lead  $R_{\theta JL}$  and lead to ambient.
- D. The static characteristics in Figures 1 to 6 are obtained using  $t \leq 300\mu s$  pulses, duty cycle 0.5% max.
- E. These tests are performed with the device mounted on 1 in2 FR-4 board with 2oz. Copper, in a still air environment with  $T_A=25^\circ C$ . The SOA curve provides a single pulse rating.
- F. The current rating is based on the  $t \leq 10s$  thermal resistance rating.
- G.  $E_{AR}$  and  $I_{AR}$  ratings are based on low frequency and duty cycles to keep  $T_j=25^\circ C$ .

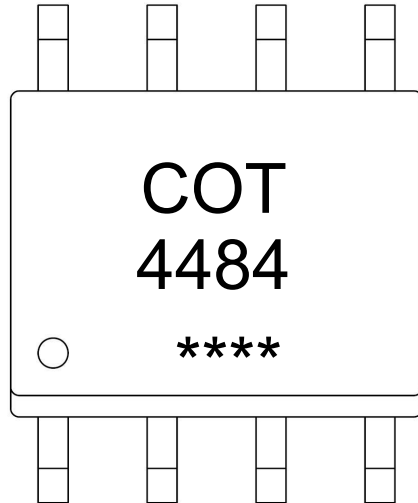
Electrical Characteristic Curve



Electrical Characteristic Curve



**Marking Instructions**



Note:

COT: Company Code.

4484: Product Type.

\*\*\*\*: Lot No. Code, code change with Lot No.

**Packaging SPEC**

REEL

Package Type	Units					Dimension (unit: mm <sup>3</sup> )		
	Units/Reel	Reels/Inner Box	Units/Inner Box	Inner Boxes/Outer Box	Units/Outer Box	Reel	Inner Box	Outer Box
SOP/ESOP-8	4,000	2	8,000	6	48,000	13" x12	360×360×50	380×335×366

**Package Dimensions**

SOP-8

Unit:mm

