

### Descriptions

This ±30V N-Channel and P-Channel complementary Enhancement MOSFET in a SOP-8 Plastic Package.

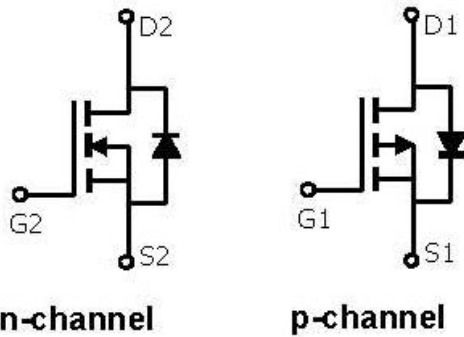
### Features

- |                                       |                                      |
|---------------------------------------|--------------------------------------|
| ● N-channel                           | P-channel                            |
| ● $V_{DS}(V)=30V$                     | $V_{os}(V)=-30V$                     |
| ● $I_D=6.9A$                          | $I_b=-6A$                            |
| ● $R_{DS(ON)}<32m\Omega(V_{GS}=10V)$  | $R_{os(ON)}<65m\Omega(V_{Gs}=-10V)$  |
| ● $R_{DS(ON)}<36m\Omega(V_{GS}=4.5V)$ | $R_{os(ON)}<75m\Omega(V_{gs}=-4.5V)$ |
| ● Halogen-free Product.               |                                      |

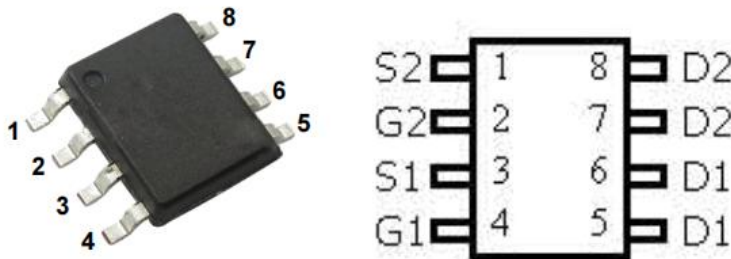
### Applications

These devices are well suited for high efficiency switching DC/DC converters and switch mode power supplies. And suitable for use as a load switch or in PWM applications.

### Equivalent Circuit



### Pinning



Absolute Maximum Ratings(T<sub>A</sub>=25°C)

Parameter	Symbol	Rating		Unit
		N-channel	P-channel	
Drain-Source Voltage	V <sub>DSS</sub>	±30		V
Gate-Source Voltage	V <sub>GSS</sub>	±12		V
Continuous Drain Current <sup>A</sup>	I <sub>D</sub> (T <sub>A</sub> =25°C)	6.9	-6.0	A
	I <sub>D</sub> (T <sub>A</sub> =70°C)	5.8	-5.0	A
Pulsed Drain Current <sup>B</sup>	I <sub>DM</sub>	±30		A
Power Dissipation	P <sub>D</sub> (T <sub>A</sub> =25°C)	2		W
	P <sub>D</sub> (T <sub>A</sub> =70°C)	1.44		W
Maximum Junction-to-Ambient <sup>A</sup>	R <sub>θJA</sub> (t≤10s)	62.5		°C/W
	R <sub>θJA</sub>	110		°C/W
Maximum Junction-to-Lead <sup>C</sup>	R <sub>θJL</sub>	60	4	°C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150		°C

## Notes:

A: The value of R<sub>θJA</sub> is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with T<sub>A</sub> =25°C. The value in any a given application depends on the user's specific board design. The current rating is based on the t ≤ 10s thermal resistance rating.

B: Repetitive rating, pulse width limited by junction temperature.

C: The R<sub>θJA</sub> is the sum of the thermal impedance from junction to lead R<sub>θJL</sub> and lead to ambient.

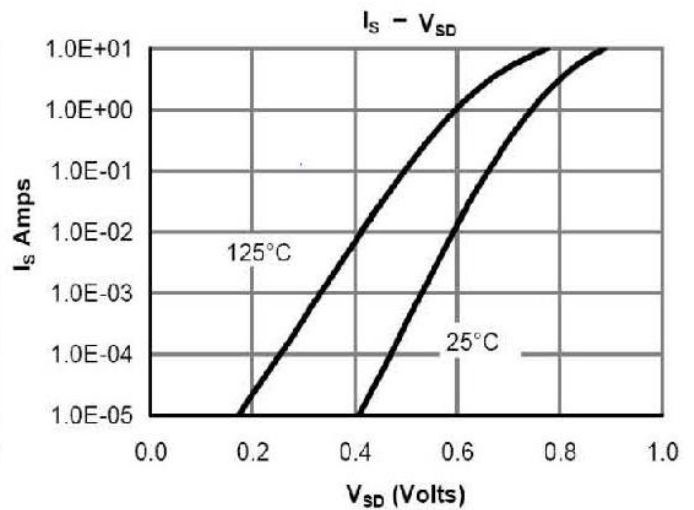
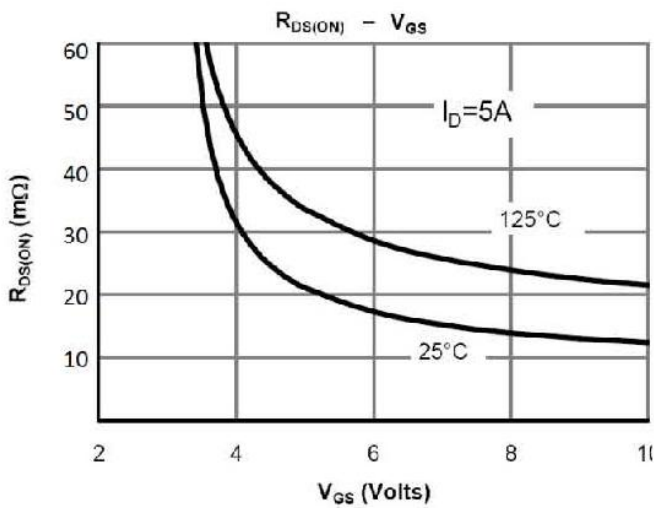
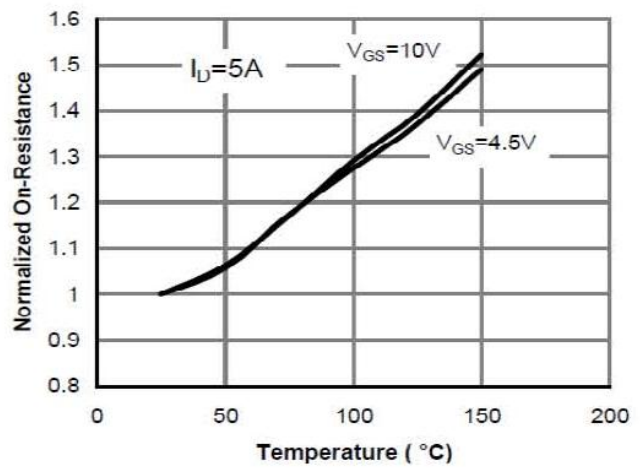
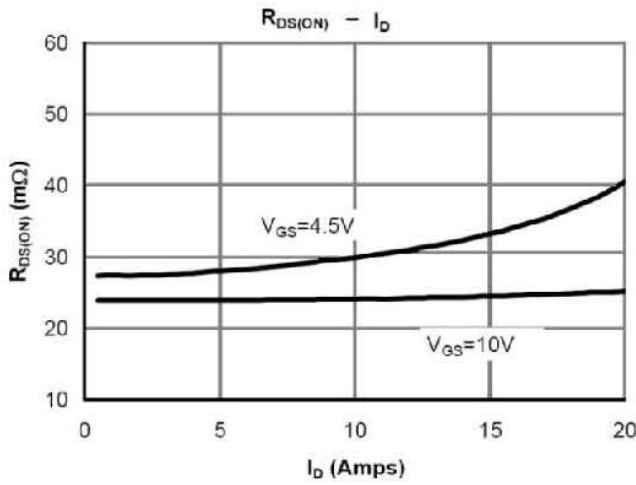
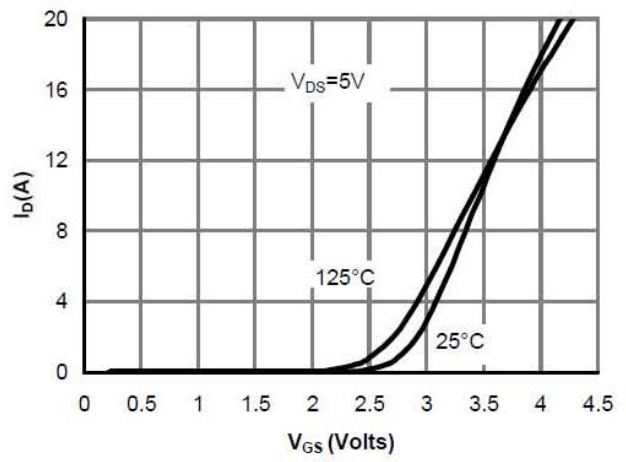
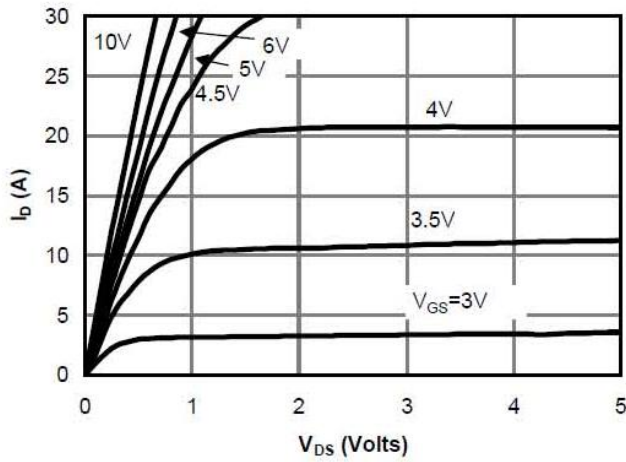
D: The static characteristics in Figures 1 to 6,12,14 are obtained using 80 μs pulses, duty cycle 0.5% max.

E: These tests are performed with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with T<sub>A</sub>=25°C. The SOA curve provides a single pulse rating.

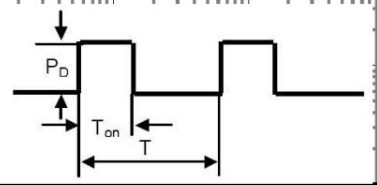
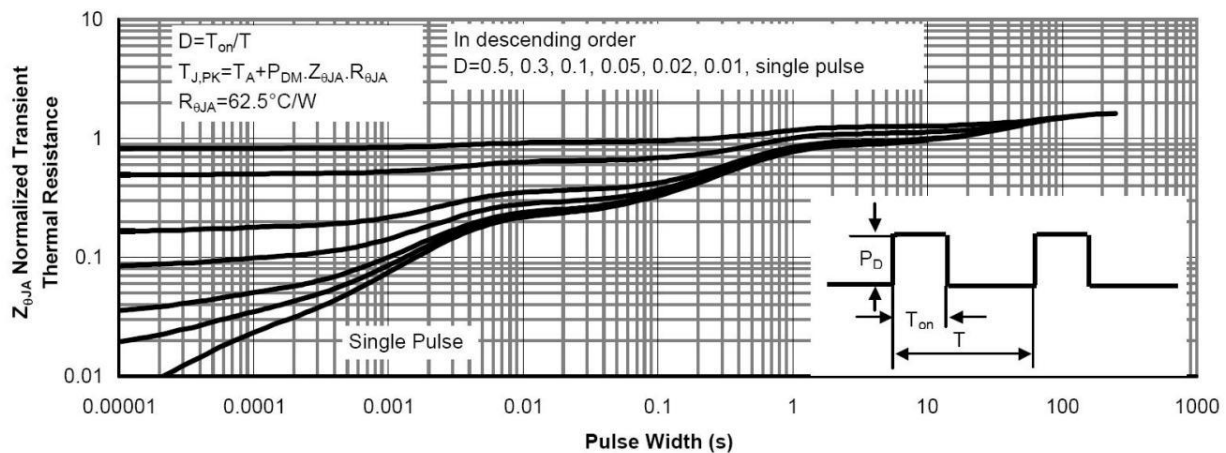
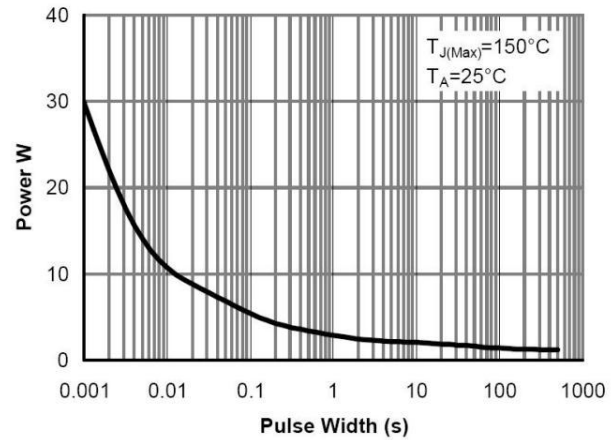
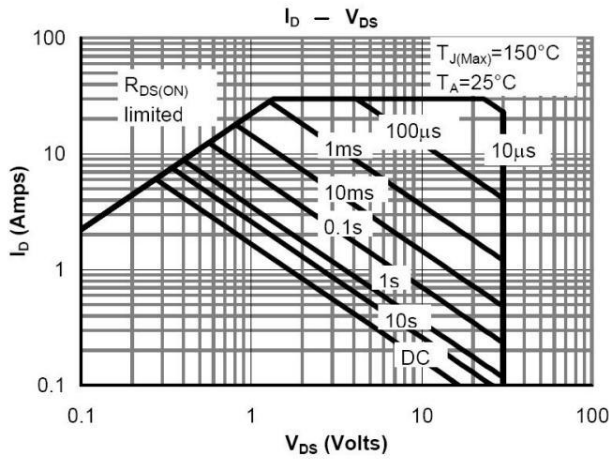
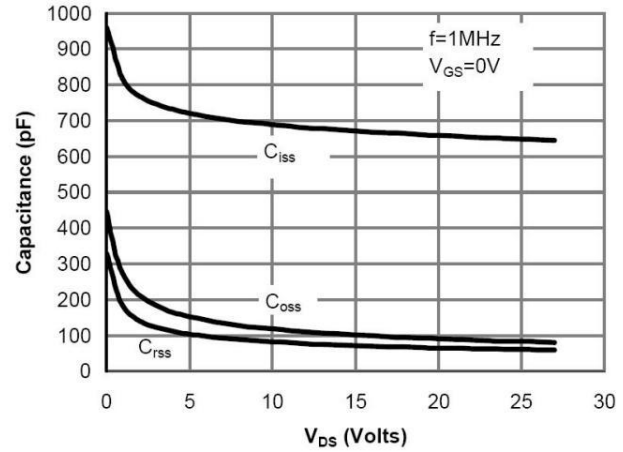
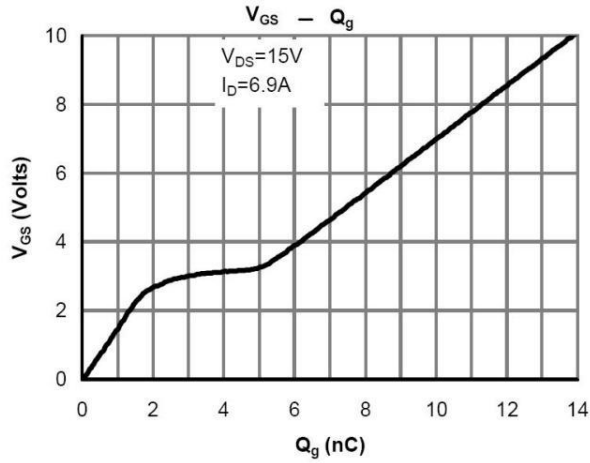
Electrical Characteristics(Ta=25°C)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	30			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =24V V <sub>GS</sub> =0V			1.0	μA
		V <sub>DS</sub> =24V V <sub>GS</sub> =0V T <sub>J</sub> =55°C			5.0	μA
Gate-Body leakage current	I <sub>GSS</sub>	V <sub>GS</sub> =±12V V <sub>DS</sub> =0V			100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> I <sub>D</sub> =250μA	0.65		1.3	V
On state drain current	I <sub>D(on)</sub>	V <sub>DS</sub> =4.5V V <sub>GS</sub> =5.0V	6.9			A
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V I <sub>D</sub> =6.9A		24	32	mΩ
		V <sub>GS</sub> =10V I <sub>D</sub> =6.9A T <sub>J</sub> =125°C		32.3	38	mΩ
		V <sub>GS</sub> =4.5V I <sub>D</sub> =5.0A		27	36	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =5.0V I <sub>D</sub> =5.0A		9		S
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V I <sub>S</sub> =1.0A		0.76	1.0	V
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =15V V <sub>GS</sub> =0V f=1.0MHz		680		pF
Output Capacitance	C <sub>oss</sub>			102		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			77		pF
Gate resistance	R <sub>g</sub>	V <sub>DS</sub> =0V V <sub>GS</sub> =0V f=1.0MHz		3.0		Ω
Total Gate Charge(10V)	Q <sub>g</sub>	V <sub>GS</sub> =10V V <sub>DS</sub> =15V I <sub>D</sub> =6.9A		13.84		nC
Total Gate Charge(4.5V)				6.74		nC
Gate-Source Charge	Q <sub>gs</sub>			1.82		nC
Gate-Drain Charge	Q <sub>gd</sub>			3.2		nC
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DS</sub> =15V V <sub>GS</sub> =10V R <sub>L</sub> =2.1Ω R <sub>GEN</sub> =3Ω		4.6		ns
Turn-On Rise Time	t <sub>r</sub>			4.1		ns
Turn-Off Delay Time	t <sub>d(off)</sub>			20.6		ns
Turn-Off Fall Time	t <sub>f</sub>			5.2		ns
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =6.9A di/dt=100A/μs		16.5		ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> =6.9A di/dt=100A/μs		7.8		nC

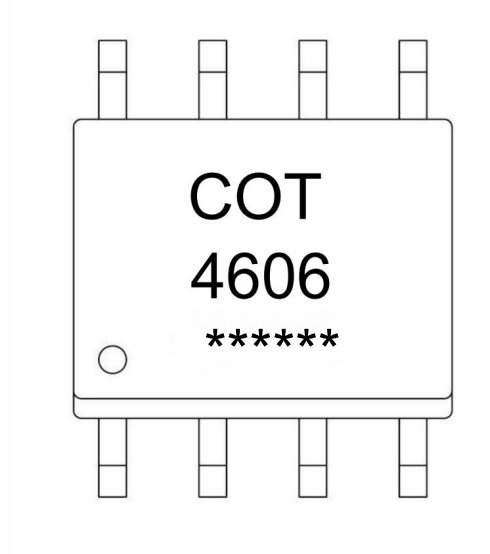
Electrical Characteristic Curve



Electrical Characteristic Curve



**Marking Instructions**



- Note:
- COT: Company Logo
  - 4606: Product Type.
  - \*\*\*\*\*: Lot No. Code, code change with Lot No.

**Packaging SPEC**

REEL INFORMATION

Package Type	Units					Dimension (unit: mm <sup>3</sup> )		
	Units/Reel	Reels/Inner Box	Units/Inner Box	Inner Boxes/Outer Box	Units/Outer Box	Reel	Inner Box	Outer Box
SOP-8	4,000	2	8,000	6	48,000	13" ×12	360×360×50	380×335×366

Package Outline Dimensions

SOP-8

Unit:mm

