Descriptions

This N-Channel Enhancement Mode Field Effect Transistor in a PDFN 3×3A-8L Plastic Package

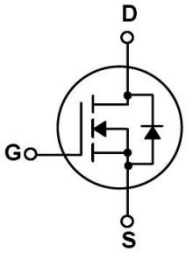
Features

- V_{DS} (V) = 40V I_D =57A (V_{GS} = ±20V)
- $R_{DS(ON)}$@10V≤5mR(Typ.4.5mR)
- $R_{DS(ON)}$@4.5V≤10mR(Typ.6.2mR)
- Halogen-Free Product

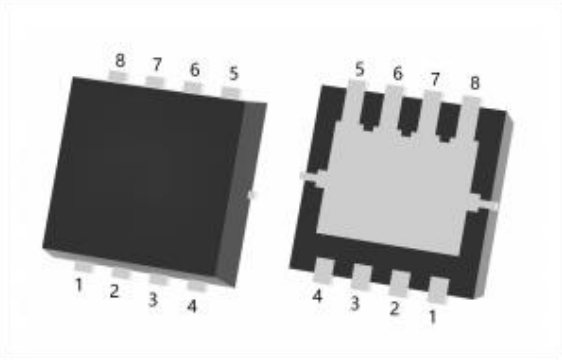
Applications

- Load Switch Applications
- Battery Power Management.

Equivalent Circuit



Pinning



出脚	定义
Pin1	S
Pin2	S
Pin3	S
Pin4	G
Pin5	D
Pin6	D
Pin7	D
Pin8	D

Marking

See Marking Instructions.

Absolute Maximum Ratings(Ta=25℃)

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V_{DSS}	40	V
Drain Current		I_D(Tc=25℃)	57	A
Drain Current - Pulsed		I_{DM}	137	A
Gate-Source Voltage		V_{GSS}	±20	V
Single Pulsed Avalanche Energy		E_{AS}	583.2	mJ
Avalanche Current		I_{AS}	27	A
Power Dissipation		P_D(Tc=25℃)	30	W
Operating and Storage Temperature Range		T_J,T_{stg}	-55 to 150	℃
Junction-to-Ambient	t≤10	$R_{\theta JA}$	40	℃/W
Junction-to-Ambient	Steady-State		75	
Junction-to-Case	Steady-State	$R_{\theta JC}$	4.2	

Electrical Characteristics(Ta=25℃)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V I_D=250μA	40	43		V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=40V V_{GS}=0V			1	μA
Gate-Body Leakage Current Forward	I_{GSS}	V_{GS}=±20V V_{DS}=0V			±0.1	μA
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS} I_D=250μA	1.0	1.7	2.5	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	V_{GS}=10V I_D=20A		4.5	5	mΩ
		V_{GS}=4.5V I_D=10A		6.2	10	mΩ
Drain-Source Diode Forward Voltage	V_{SD}	V_{GS}=0V I_S=1A			1.2	V
Input Capacitance	C_{iss}	V_{DS}=25V V_{GS}=0V f=1.0MHz		2900		pF
Output Capacitance	C_{oss}			210		
Reverse Transfer Capacitance	C_{rss}			200		
Gate resistance	R_g	V_{GS}=0V V_{DS}=0V f=1MHz		2.5		Ω
Total Gate Charge	$Q_{g(10V)}$	V_{GS}=10V V_{DS}=20V I_D=20A		70		nC
Total Gate Charge	$Q_{g(4.5V)}$			15		
Gate Source Charge	Q_{gs}			15		
Gate Drain Charge	Q_{gd}			22		
Turn-On Delay Time	$t_{d(on)}$	V_{GS}= 10V V_{DS}=20V R_{GEN}=3.0Ω R_L=1 Ω		15		ns
Turn-On Rise Time	t_r			30		
Turn-Off Delay Time	$t_{d(off)}$			54		
Turn-Off Fall Time	t_f			20		

Electrical Characteristic Curve

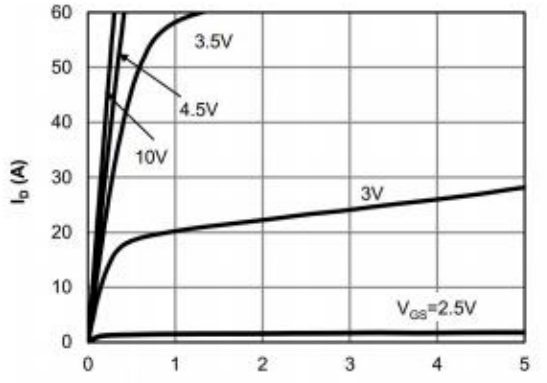


Figure 1: On-Region Characteristics

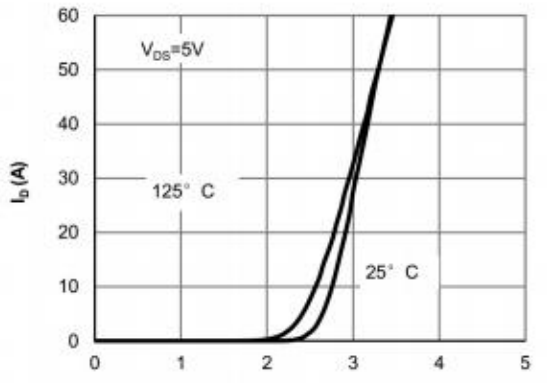


Figure 2: Transfer Characteristics

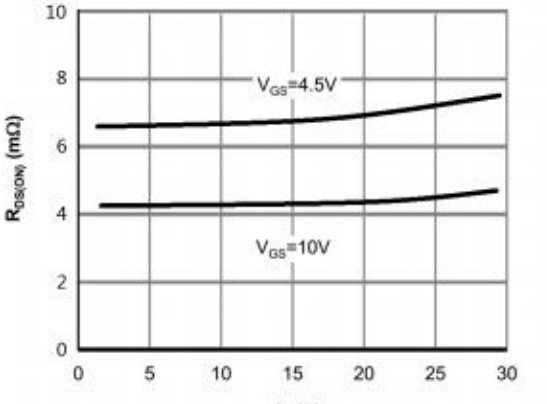


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

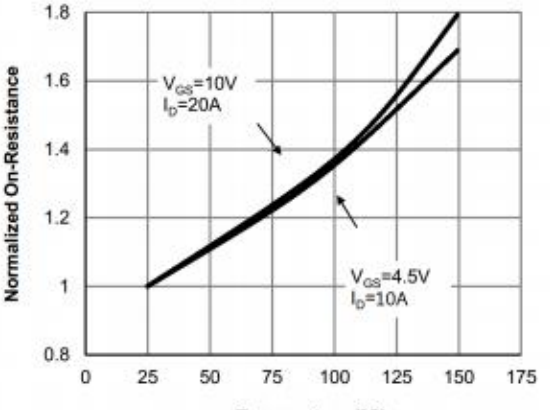


Figure 4: On-Resistance vs. Junction Temperature

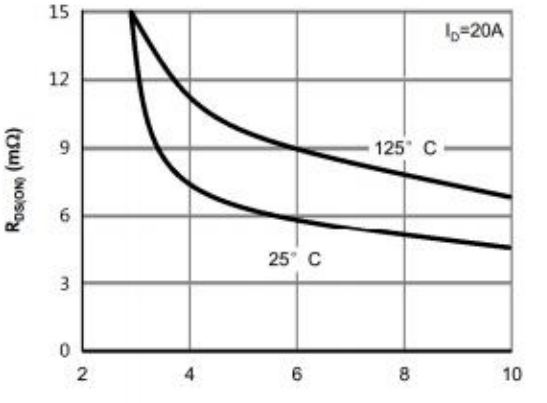


Figure 5: On-Resistance vs. Gate-Source Voltage

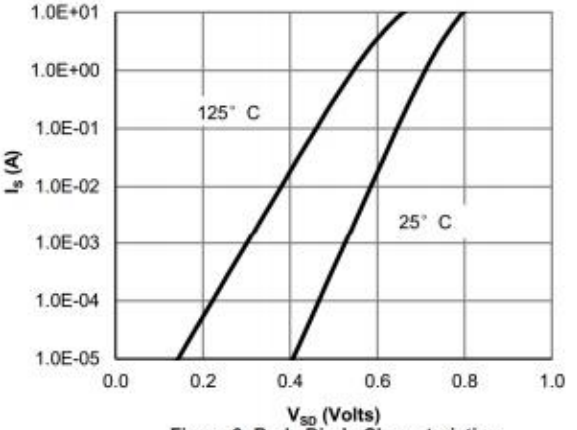


Figure 6: Body-Diode Characteristics

Electrical Characteristic Curve

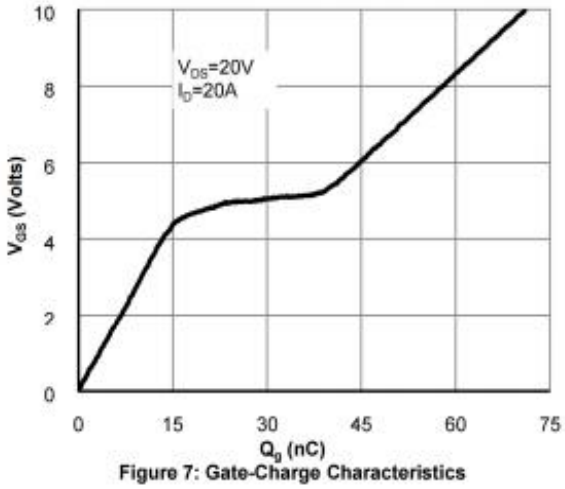


Figure 7: Gate-Charge Characteristics

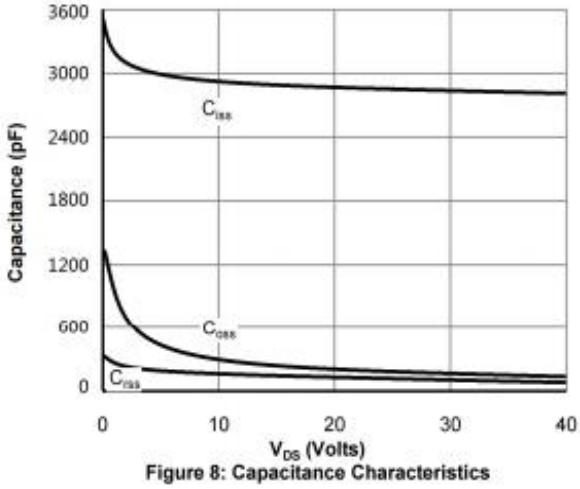


Figure 8: Capacitance Characteristics

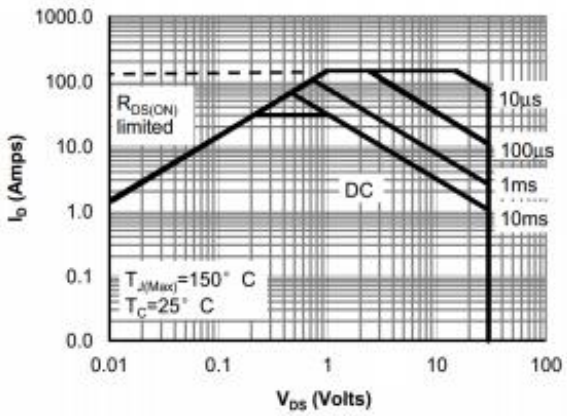


Figure 9: Maximum Forward Biased Safe Operating Area

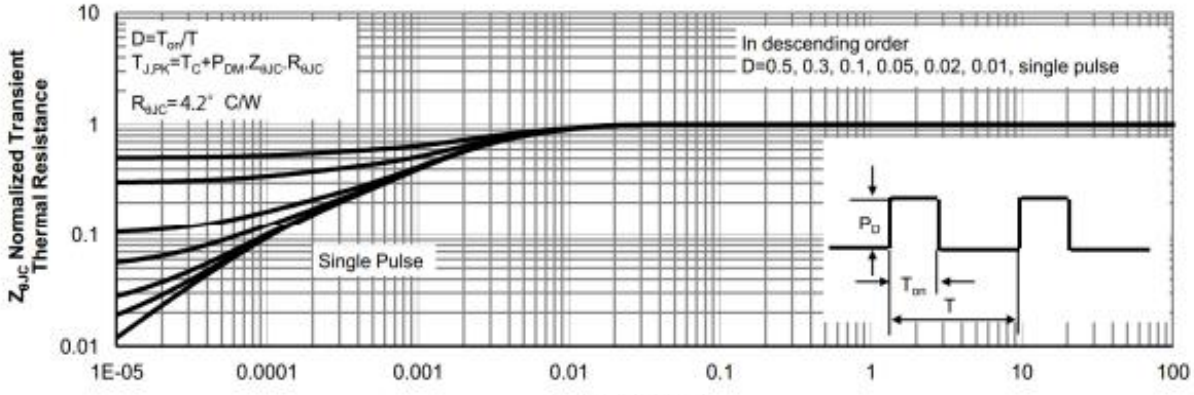
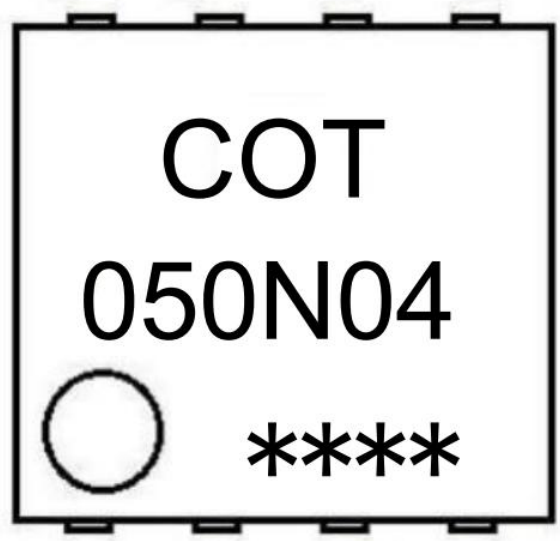


Figure 10: Normalized Maximum Transient Thermal Impedance

Marking Instructions



Note:

COT:	Company Logo
050N04:	Product Type.
****:	Lot No. Code, code change with Lot No.

Packaging SPEC

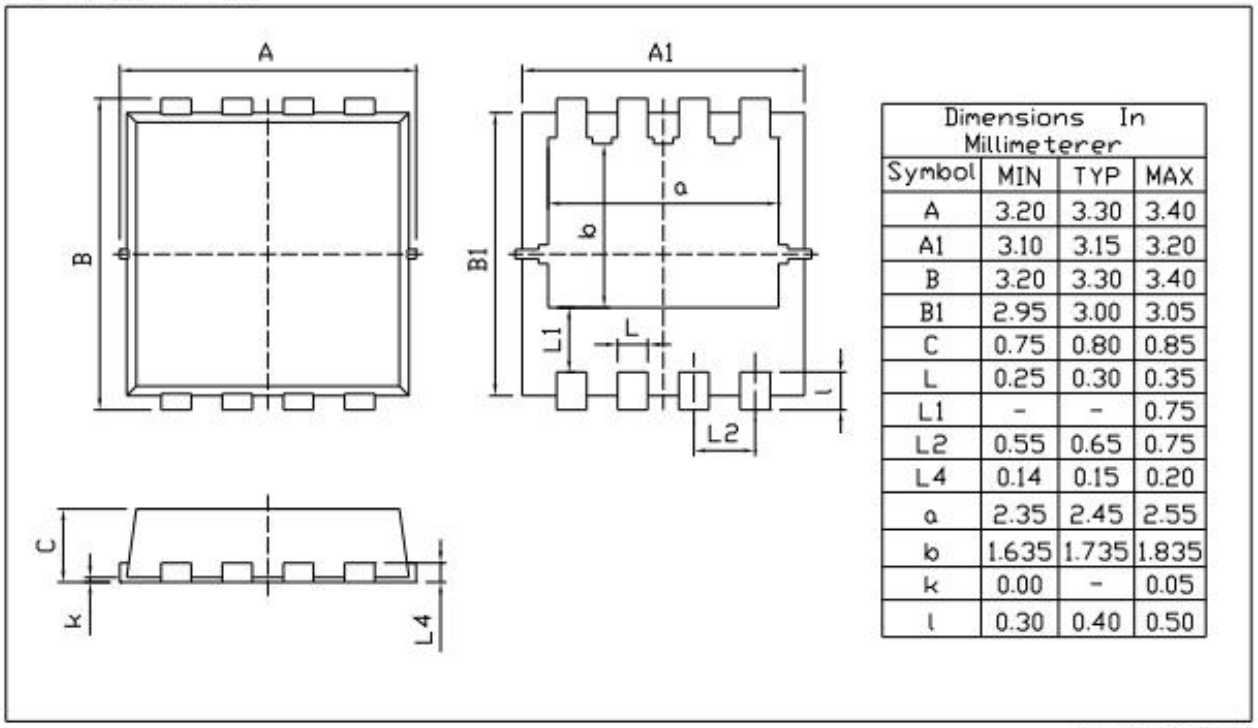
REEL INFORMATION

Package Type	Units					Dimension (unit: mm³)		
	Units/Reel	Reels/Inner Box	Units/Inner Box	Inner Boxes/Outer Box	Units/Outer Box	Reel	Inner Box	Outer Box
PDFN 3×3A-8L	5,000	2	10,000	6	60,000	13″ ×12	360×360×50	380×335×366

Package Outline Dimensions

PDFN3X3A-8L

Unit:mm



Dimensions In Millimeterer			
Symbol	MIN	TYP	MAX
A	3.20	3.30	3.40
A1	3.10	3.15	3.20
B	3.20	3.30	3.40
B1	2.95	3.00	3.05
C	0.75	0.80	0.85
L	0.25	0.30	0.35
L1	-	-	0.75
L2	0.55	0.65	0.75
L4	0.14	0.15	0.20
a	2.35	2.45	2.55
b	1.635	1.735	1.835
k	0.00	-	0.05
l	0.30	0.40	0.50

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